Nano Technology

Preparation and Enhancement of CdS/ZnS Thin Films for Photovoltaic Purposes

Maysa Tayseer Mohammad Atatrih Chemistry Department, An-Najah National University Nablus, Palestine Tel: 04-2416436

Abstract

Polycrystalline CdS/ZnS thin films were prepared by chemical bath deposition (CBD) technique on fluorine-doped tin oxide (FTO) coated glass substrates. Enhancement of deposited CdS/ZnS thin film characteristics at solid/liquid interface in photoelectrochemical (PEC) systems was investigated. Deposited CdS/ZnS thin films were exposed to different treatment methods and different treatment methods and different treatment methods and different experimental conditions. The films were heated to desired temperatures (300°C, 400°C) under air. Cooling of heated films to room temperature

was achieved by either slow cooling or quenching. Etching of film surface was conducted using dilute HCI solution. The effect of such treatment on the film photoelectrochemical characteristics was measured by monitoring different parameters, such as: open-circuit voltage (Voc), short-circuit current density (Jsc), dark current density-potential (J-V) plots, photo J-V plots, conversion efficiency (η), fill factor (FF), Scanning electron microscopy (SEM), X-ray diffraction, together with electronic absorption and photoluminescence (PL) emission spectra.

The characteristics of CdS/ZnS thin films in PEC systems were enhanced by controlling different experimental conditions, controlling preheating temperatures and controlling cooling rates. The dark- and photo- current densities vs. potential plots were improved by annealing. Cell efficiency, fill factor, short-circuits current densities (Jsc) and SEM results were enhanced for the annealed CdS/ZnS films. The best annealing temperature for CdS/ZnS films was found to be 300°C at which the photo J-V plots and cell efficiency were improved significantly. Slowly cooled electrodes from temperature 300°C, gave better dark and photo current density vs. potential plots with higher efficiency than their quenched counterparts. SEM measurements were consistent with these findings, and showed better surfaces for

(IYC-2011) 69

slowly cooled CdS/ZnS thin film electrodes. Maximum values of conversion efficiencies were obtained by slow cooling of preheated CdS/ZnS electrodes cooled from temperature 300°C compared to that of electrodes cooled from 400°C.

The effect of coating the CdS/ZnS electrodes with MnP/polysiloxane was also studied. The (Jsc) values of coated CdS/ZnS films (with certain Zn ratios) were significantly enhanced. The MnP/ploysilocane coating introduces a charge-transfer mediator species that enhances current and electrode stability.